

# Tarik Saidani

## Curriculum Vitae

**Contact**            The Institute of Fundamental Electronics  
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### Research Interests

Code optimization, parallel computing, image processing, computer architecture.

### Education

*Ph.D., Computer Science*, expected Fall 2009  
University of Paris Sud 11  
Thesis: *Computer Vision Codes Optimization for Embedded Systems*  
Advisers: Dr. Lionel Lacassagne and Pr. Alain Mériqot  
  
*M.S., Electrical Engineering*, September 2006  
University of Paris Sud 11  
Thesis: *SIMD ISA Customization for Embedded Systems*  
Adviser: Dr. Lionel Lacassagne  
  
*B.S., Electrical Engineering*, June 2005  
University of Paris Sud 11

### Publications

T.Saidani, S. Piskorski, Lionel Lacassagne and Samir Bouaziz, *Efficient AltiVec Customization for Embedded Systems*. ICTIS '07: Proceedings of the 2007 Information and Communication Technologies International Symposium, April 2007.

T. Saidani, L. Lacassagne, S. Bouaziz and T. M. Khan, *Parallelization Strategies for the Points of Interests Algorithm on the Cell Processor*. ISPA '07: Proceedings of the 5th International Symposium on Parallel and Distributed Processing and Applications, August 2007.

T. Saidani, S. Piskorski, Lionel Lacassagne and S.Bouaziz, *Parallelization Schemes for Memory Optimization on the Cell Processor: A Case Study of Image Processing Algorithm*. MEDEA '07: Proceedings of the 2007 workshop on MEMory performance, in conjunction with PACT'07, September 2007.

## **Technical Skills**

### **Proficient**

C, AltiVec ISA, SPU ISA, SSE ISA,  $\LaTeX$ , Matlab.

### **Familiar**

C++, Cg, OpenGL, CUDA, SystemC, VHDL, Perl, XHTML, CSS.

## **Languages**

**Arabic** : mother tongue

**French** : bilingual

**English** : written, spoken and read (840/990 at TOEIC test)

## **Miscellaneous**

*Citizenship:* Algeria

*Marital Status:* Single

*Age:* 26

February 14, 2008